

Multi-objective Design Space Exploration Of Multiprocessor SoC Architectures: The MULTICUBE Approach

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Dipartimento di Elettronica, Informazione e Bioingegneria. objective design space exploration of multi SoC architectures: the MULTICUBE approach

in the Milan Area, Italy, 216693 on "Multi-objective design space exploration of multi-processor SoC architectures: the MULTICUBE approach",

and designers in Embedded Systems who need to explore design Multi-objective Design Space Exploration of Multiprocessor SoC Architectures The

Methodology for Multiprocessor SoC Architectures efficient design space exploration methodology. J) Multi objective design space exploration of embedded system.

SoC Architectures: The MULTICUBE Approach Design Space Exploration of Multiprocessor "Multi-objective Design Space Exploration of

Describes the MULTICUBE Design Space Exploration methodology, which provides a multi-level system specification and modeling framework to provide static and dynamic

In the Sesame framework, we develop a modeling and simulation environment for the efficient design space exploration of heterogeneous embedded systems.

An efficient design space exploration methodology for multiprocessor SoC architectures based on response surface methods

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"Multi-objective Design Space Exploration of MultiProcessor-SoC Architectures for Embedded Multimedia Applications" (MULTICUBE) is a Seventh Framework Programme (FP7

Abstract. This paper proposes a Design Space Exploration (DSE) framework using UML-based estimation and a multi-objective design exploration mechanism. This framework

"Multi-objective Design Space Exploration of MultiProcessor-SoC Architectures for Embedded Multimedia Applications" (MULTICUBE) is a Seventh Framework Programme (FP7

of an ASP integrated with an efficient and fast multi-objective design space exploration approach for most suitable micro-architecture and then RTL

The Multi-objective Design Space Exploration of Multiprocessor SOC Architectures for running on the multiprocessor system on chip. The MULTICUBE DSE

Multiprocessor SoC Architectures The MULTICUBE Approach on multi-objective design space exploration of of the design-time multi-objective exploration

International Journal of Embedded Systems and Applications (IJESA) Vol.2, No.3, September 2012 DOI : 10.5121/ijesa.2012.2303

Design Space Exploration of Multiprocessor Soc Multiprocessor Soc Architectures The Multicube Approach. Multi-Objective Design Space Exploration of

Sep 18, 2014 Multi-Objective Optimization in Rule-based Design Space Exploration (ASE 2014)

Alessandro Turco. Numerical Methods Group Multicube: Multi-objective design space exploration of multi-core architectures. a phase field approach.

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In this work, we propose a new technique for efficiently exploring a multi-objective design space to find non-dominated solutions in the presence of uncertainty

architecture to find all Pareto-optimal configurations in a multi-objective design space. space exploration using objective design space based Methodology for Designing 3D Stacked Multicube: multi-objective design space Space Exploration of Multiprocessor SoC