

Intel Xeon Phi Coprocessor High-Performance Programming By James Reinders

By James Reinders

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in which the authors claim that achieving high performance with Xeon Phi still Programming tools include with the announcement of Intel Xeon Phi

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a C code taken from Intel Xeon Phi Coprocessor High Performance Programming by Jim Jeffers and James Reinders code for the MIC coprocessor: Intel's

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Purpose This code recipe describes how to get, build, and use the NAMD* Scalable Molecular Dynamics code for the Intel Xeon Phi Coprocessor.

James Reinders is the (Intel Xeon Phi coprocessor). James been an (Morgan Kaufmann, 2012), Intel Xeon Phi Coprocessor High Performance Programming

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